

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10073877	FILING DATE 02/14/2002	CLASS 438	SUBCLASS 590	GAU 2012	EXAMINER 2822, GUERRERO
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**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:
JAPAN 2001-039252 02/15/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 020166
TITLE : Process of manufacturing a semiconductor device			
U.S.DEP.T. OF COMM./PAT.& TM-PTO-436 (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims <input type="checkbox"/> Print Claims for 0.0	
		DRAWING Sheets Drawn. <input type="checkbox"/> Figs. Drawn. <input type="checkbox"/> Print Figs. <input type="checkbox"/>	
		Primary Examiner	
		PREPARED FOR ISSUE Application Examiner	
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